

**AMENDMENTS TO THE SPECIFICATION**

Please amend paragraph [0023] of the specification as follows:

**[0023]** The code would then send an interrupt to all slave processors 414, 415. A slave processor is all of the processors of the partition, except for the processor executing the reset code. Slave processors 414 exist on the monarch cell 402a, which is the cell that includes the processor executing the reset code, i.e. the monarch processor 412. Slave processors 415 also exist on the slave cells 402b 410b, 402c, which are the remaining cells of the partition, other than the monarch cell. The interrupt would inform each cell that it is about to be reset. The interrupting code writes a predefined value to a control status register (CSR), regnum 310 on the cell controller. When the slave processors receive the interrupt, by convention, they read this register to see what action they should take as a result of being interrupted. They will see the status indicator that a partition reset is about to occur and go to an idle loop, waiting to be reset. One example of an interrupt is CELLST\_RESET\_MODE.